Average Model of Boost Converter, including Parasitics, operating in Discontinuous Conduction Mode (DCM)

Haytham Abdelgawad and Vijay Sood
Faculty of Engineering and Applied Science, University of Ontario Institute of Technology (UOIT), Oshawa, Canada
haytham.abdel-gawad@uoit.ca

Abstract—This paper presents the principles of state-space average modeling of DC-DC Boost converters operating in the Discontinuous Conduction Mode (DCM) which occurs in converters due to low load current operation. The switching ripples in inductor current or capacitor voltage cause polarity reversal of the applied switch current or voltage and thus a zero current mode is reached. Modern converters are often designed to operate in DCM for all loads due to their higher efficiency. In this paper, the reduced-order & full-order average models for the Boost converter are derived. Also, the output transfer function of the Boost converter is calculated which can be further utilized for designing a robust controller. Furthermore, the effects of parasitic elements and losses are included based on the state-space averaging technique. The open-loop transfer functions of the proposed models are derived and the behavior of the Boost converter is verified by analysing transient step responses.

Index Terms—Boost converters, Discontinuous conduction mode, Full-order model, Parasitic realization, Reduced-order model, State-space average modeling.

NOMENCLATURE

\[ T_s \] \quad \text{Total time period for one cycle}
\[ t_{on} \] \quad \text{ON-time period}
\[ d_i \] \quad \text{Duty cycle}
\[ d_i T_s \] \quad \text{ON-time period}
\[ d_i T_p \] \quad \text{OFF-time period}
\[ i_{pk} \] \quad \text{Peak value of inductor current}
\[ i_{av} \] \quad \text{Average value of inductor current}
\[ x \] \quad \text{state vector}
\[ A_1 \] \quad \text{state (or system) matrix during ON-state}
\[ B_1 \] \quad \text{input matrix during ON-state}
\[ A_2 \] \quad \text{state (or system) matrix during OFF-state}
\[ B_2 \] \quad \text{input matrix during OFF-state}
\[ A_3 \] \quad \text{state (or system) matrix during DCM}
\[ B_3 \] \quad \text{input matrix during DCM}
\[ n_k \] \quad \text{No. of inductors in the converter circuit}
\[ K \] \quad \text{Correction matrix}

\[ V_i \] \quad \text{Small perturbation value of inductor current}
\[ V_c \] \quad \text{Small perturbation value of capacitor voltage}
\[ V_o \] \quad \text{Small perturbation value of input voltage}
\[ M \] \quad \text{Output to input voltage ratio}

I. INTRODUCTION

Among the different topologies of DC-DC converters [1], the Boost converter with typical efficiencies of 70-95% [2], is the one where the output voltage is always greater than its input voltage. By comparing the Boost converter with the Buck or Buck-Boost converters, it is found that the design of the Boost converter is more difficult since the Boost converter is considered as a non-minimum phase system and also has a zero root in the right half of the s-plane. In other words, as a result of the duty cycle, which is the control input for this converter and appears in the current and voltage equations, the solution of the state-space equations of this converter is more difficult [3].

The structure of DC-DC converters consists of linear (i.e. resistor R, inductor L and capacitor C) and nonlinear (i.e. switch) components. Since these converters can be characterized as nonlinear and time-variant systems, then the small-signal model of the state-space average model is required to design a linear controller. References [4-6] show examples of the small-signal analysis and design of a linear controller in the frequency domain for DC-DC converters.

Consideration of all of the system parameters (such as conduction resistances, switch conduction voltages, switching times, and inductor- and capacitor-resistances) in the modelling procedure is an essential step towards the design of a robust controller. In [7-9] Basso, Tomescue and Towati considered the inductor- and capacitor-resistances and the output current in the modelling procedure of the Boost converter. In [10], Ben-Yaakov considered the capacitor resistance and the output current in the modelling procedure of the Boost converter. In [11, 12], a Pulse Width Modulated (PWM) converter with ideal system parameters, both in continuous and discontinuous conduction modes, is modelled and the effect of conduction resistances is included for the same model. In [13], the average model of the PWM converter
is demonstrated by considering the conduction resistances and their voltage drop in DCM.

This paper presents DC and small-signal circuit models of the DC-DC Boost converters operating in DCM. The parasitic components are included in these models to improve the modelling accuracy. The DC model is used to extract the equations for the DC voltage transfer function and the efficiency. The small-signal model is used to extract the equations for the open-loop small-signal transfer functions, such as control to output transfer function, input to output transfer function, and input impedance.

Finally, the various transfer functions (Control, Output Impedance, etc.), various Bode diagrams and transient step responses have been plotted and compared with ideal cases.

II. BOOST REGULATOR STATE EQUATIONS FOR ON-OFF TIME SWITCHING

For modelling with the state-space technique, the desired state variables for any electric circuit are the energy storing elements (i.e. inductor current and capacitance voltage). To start applying the state-space technique to any complicated circuit, it must first be converted into a piece-wise linearized circuit(s) in which circuit laws can be applied.

In the Boost converter (Fig.1), there are two operating modes; the ON- and OFF-modes. The ON-time is defined by \( d_1T_s \), and the OFF-time is defined by \( d_2T_s = (1-d_1)T_s \), where \( T_s \) is the total time period for one cycle and \( d_1 \) is the duty cycle which is defined as the ratio of the ON-time period to the total switching time period for one cycle i.e. \( d_1 = t_{on} / T_s \).

The main switch \( S \) is turned ON and OFF by a pulse with a duty cycle equal to \( d_1 \). Therefore, the piece-wise linearized equivalent circuits of the system in ON- and OFF-modes with duration \( d_1T_s \) and \( (1-d_1)T_s \) are represented by Figs. 2 and 3 respectively.

![Fig.1 Boost converter circuit](image)

Using \( i_L \) and \( v_C \) as the two state variables \( x = [i_L, v_C]^T \), and by writing the KVL and KCL for the loops of Figs. 2 and 3, the state-space equations can be derived:

During the ON-state, the state-space (ss) form will be:

\[
\begin{align*}
\frac{di_L}{dt} &= [0 \quad 0 \quad 1] \frac{v_C}{L} + \frac{1}{L} v_{in} \\
v_o &= [0 \quad 1] \frac{v_C}{v_C}
\end{align*}
\]  
(1)

During the OFF-state, the ss form will be:

\[
\begin{align*}
\frac{di_L}{dt} &= \frac{-1}{L} \frac{v_C}{v_C} + \frac{1}{L} v_{in} \\
v_o &= [0 \quad 1] \frac{v_C}{v_C}
\end{align*}
\]  
(2)

![Fig.2 Equivalent circuit of Boost converter during ON-state](image)

![Fig.3 Equivalent circuit of Boost converter during OFF-state](image)

Fig.4 shows the steady-state waveforms for the Continuous Conduction Mode (CCM) where the inductor current flows continuously \( [i_L(t) > 0] \).
In Discontinuous Conduction Mode (DCM), in addition to two modes as in Continuous Conduction Mode (CCM), there is a third mode of operation in which capacitor voltage or inductor current is zero. For DCM operation, during first interval (i.e. ON-period) the switch is turned on and inductor current rises and reaches a peak when the switch is about to turn off, and resets to zero at the end of the OFF-period. During DCM, in ss form:

\[
\begin{bmatrix}
\frac{d i_L}{dt} \\
\frac{d v_C}{dt}
\end{bmatrix} =
\begin{bmatrix}
0 & 0 \\
-\frac{1}{RC} & 0
\end{bmatrix}
\begin{bmatrix}
i_L(t) \\
v_C(t)
\end{bmatrix}
+ \begin{bmatrix}
0 \\
\frac{E}{R}
\end{bmatrix} v_{in}
\]

(5)

\[v_{in} = \begin{bmatrix}
0 \\
0
\end{bmatrix} \begin{bmatrix}
i_L(t) \\
v_C(t)
\end{bmatrix}
\]

(6)

\[i_L = \frac{i_{pk}}{2} (d_1 + d_2)
\]

(11)

Consider the circuit behavior when the switch is ‘OFF’; the current which is delivered to the capacitor does not necessarily have the same value as the average inductor current. Since the inductor current changes slowly with time, the capacitor equation can be solved employing the ‘conservation of energy’ principle, and after that the averaging step is performed. The total amount of charge which the capacitor obtains from the inductor during switching cycle is:

\[Q_C = \frac{i_{pk} d_1 T_s}{2}
\]

(12)

Thus, average charging current would be of value:

\[\frac{Q_C}{T_s} = \frac{i_{pk} d_1}{2}
\]

(13)

When a capacitor is connected to a resistive load, then the net average charging current which is delivered to the capacitor is given by:

\[\left(\frac{i_{pk} d_1}{2} \right) - \left( \frac{\Delta v}{R} \right)
\]

(14)

Hence, the average capacitor current will be given by:

\[\Delta v = V_{in}
\]
\[
C \frac{dv_c}{dt} = \left( \frac{dL}{2} \right) + \frac{v_c}{R}
\]

Note here that the above expression differs from the KCL expression of capacitor which is obtained through space-state averaging. From (10), we can define the state-space-average (SSA) charging current as the inductor current’s average multiplied by the duty ratio for which the inductor is charging the capacitor. From (11), the SSA charging current can be expressed as:

\[
\frac{L}{d}d_1 = \frac{L}{d}d_1 \left( \frac{d_1 + d_2}{2} \right)
\]

This expression is different from the actual charging current in (13). It can be implied that a ‘charge conservation’ law is violated in unmodified SSA as the averaging step is done on a complete model thus leading to mismatching of responses with averaged response of DC-DC converters. Thus (10) is modified by dividing the inductor current by the factor \((d_1 + d_2)\). The basic method is to rearrange the \(x\), thus \(x = [i_L, v_C]^T\), where sub-vector \(i_L\) contains all \((n_I)\) inductor currents of the converter and defined by a matrix \(K\), as below:

\[
K = d_lag \left[ \frac{1}{d_1 + d_2}, \ldots, \frac{1}{d_1 + d_2}, 1, 1, \ldots, 1 \right]_{n_I}
\]

With this correction matrix, the average modified model becomes:

\[
L = \left[ A_1 d_1 + A_2 d_2 + A_3 (1 - d_1 - d_2) Kx + B_1 x_1 + B_2 d_2 + B_3 (1 - d_1 - d_2) \right] u
\]

Where,

\[
A_1 = \begin{bmatrix} 0 & 0 \\ 0 & -1 \end{bmatrix}, A_2 = \begin{bmatrix} 0 & -1 \\ 1 & -1 \end{bmatrix} \text{ and } A_3 = \begin{bmatrix} 0 & 0 \\ 0 & 1 \end{bmatrix}
\]

\[
B_1 = \frac{1}{L}, B_2 = \frac{1}{L} \text{ and } B_3 = \begin{bmatrix} 0 \\ 0 \end{bmatrix}
\]

The SSA model for the above equation is

\[
\frac{d}{dt} [v_c] = \left[ \frac{d_1}{C} \right] \left[ \frac{L}{d} v_c \right] + \left[ \frac{d_1 + d_2}{L} \right] v_{in}
\]

Since there is only one inductor, the \(x\) is of dimension two, the correction matrix \(K\) is simply given by

\[
K = \begin{bmatrix} \frac{1}{d_1 + d_2} & 0 \\ \frac{1}{d_1 + d_2} & \frac{1}{d_1 + d_2} \end{bmatrix}
\]

Thus, the modified average model of Boost converter in DCM is given below:

\[
\frac{d}{dt} \left[ \begin{bmatrix} L \\ v_c \end{bmatrix} \right] = \left[ \begin{bmatrix} -d_2 \\ -1 \end{bmatrix} \right] \left[ \begin{bmatrix} L \\ v_c \end{bmatrix} \right] + \left[ \begin{bmatrix} d_1 + d_2 \\ 0 \end{bmatrix} \right] v_{in}
\]

\[
= \left[ \begin{bmatrix} 0 \\ -1 \end{bmatrix} \right] \left[ \begin{bmatrix} L \\ v_c \end{bmatrix} \right] + \left[ \begin{bmatrix} d_1 + d_2 \\ 0 \end{bmatrix} \right] v_{in}
\]

\[
(21)
\]

\[
III. \text{REDUCED ORDER AVERAGE MODEL FOR BOOST SWITCHING REGULATOR}
\]

To complete the average model represented in (18), a duty ratio constraint is defined showing the dependency of \(d_1\) on other variables. Usually, in conventional SSA techniques, the inductor voltage balance equation is used in defining the duty-ratio constraint.

For the Boost converter topology, utilizing the volt-second balance over the switching cycle,

\[
v_L = L \frac{d_1}{dt}
\]

\[
\Rightarrow \frac{d_1}{dt} = \frac{v_{in} - v_c}{L}
\]

For time \(T_1 = d_1 T_s\),

\[
I_{max} = \frac{v_{in} - v_c}{L} T_s
\]

For time \(T_2 = d_2 T_s\),

\[
0 - I_{max} = \frac{v_{in} - v_c}{L} T_s
\]

By removing \(I_{max}\) from above equations,

\[
I_{max} = \frac{v_{in} - v_c}{L} d_1 T_s = \frac{v_{in} - v_c}{L} d_2 T_s
\]

\[
\Rightarrow v_{in} d_1 = (v_c - v_{in}) d_2
\]

\[
\Rightarrow d_2 = \frac{v_{in}}{v_c - v_{in}} d_2
\]

Substituting \(d_2\) in (21), this will result in

\[
\frac{d}{dt} [d_1] = 0
\]

\[
\frac{d}{dt} [v_c] = \frac{v_{in} L}{v_c} - \frac{v_c}{R}
\]

\[
(24)
\]

From these calculations for a Boost converter, it can be seen that inductor current dynamics disappear, thus resulting in a degenerate model. Since inductor current is not present in the state variable in this reduced-order model, it must be replaced

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by expressing it as an algebraic function of other variables, so that inductor dynamics are removed.

For a Boost converter, the peak of the inductor current is given by:

\[
l_{pk} = \frac{v_{in}}{L} d_1 T_s
\]

The average value of inductor current is given by:

\[
l_{L} = \frac{1}{2} (d_1 + d_2) = \frac{v_{in}}{2L} (d_1 + d_2)T_s
\]

Substituting by (22), the above relationship can be written as

\[
l_{L} = \frac{v_{in}}{2L} \left( \frac{\frac{d v_{T_a}}{dt} v_{C}}{v_{C} - v_{in}} \right)
\]

Substituting (27) in (24), this will result in the conventional average model for a Boost converter in DCM. In this model, the dependency on the average inductor current is removed.

\[
\frac{dv_{C}}{dt} = \left( \frac{v_{in}^2}{2LC} \right) \left( \frac{d v_{T_a}}{dt} v_{C} \right) - \left( \frac{v_{in}}{RC} v_{in} \right)
\]

Now, applying standard linearization techniques and applying small perturbations as follows to (28):

\[
\begin{align*}
\Delta l &= \Delta L_t + \Delta L_t \\
\Delta v_c &= \Delta V_{C} + \Delta V_{C} \\
\Delta v_{in} &= \Delta V_{in} + \Delta V_{in} \\
d &= D + \Delta d
\end{align*}
\]

\[
\Delta \frac{dc_v}{dt} = \left( \left( \frac{V_{in} + V_{in}}{2LC} \right)^2 \left( \frac{(D + \Delta d)^2 T_s}{2LC} \right) \right) \left( \frac{D + \Delta d}{R} \right) \left( \frac{V_{in}}{R} \right)
\]

Separating terms of \( \Delta l_t, \Delta v_C, \Delta v_{in} \) and \( \Delta d \) and then converting it to state-space form,

\[
\begin{bmatrix} \Delta l_t \\ \Delta v_C \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ \frac{1}{RC} & \frac{2M - 1}{M - 1} \end{bmatrix} \begin{bmatrix} \Delta l_t \\ \Delta v_C \end{bmatrix} + \begin{bmatrix} \frac{M}{R(CM - 1)} \frac{D}{RC(M - 1)} \end{bmatrix} \begin{bmatrix} \Delta V_{in} \\ \frac{D}{T_s} \end{bmatrix}
\]

Which is in the form of:

\[
\frac{d}{dt} \begin{bmatrix} \Delta l_t \\ \Delta v_C \end{bmatrix} = A \begin{bmatrix} \Delta l_t \\ \Delta v_C \end{bmatrix} + B \begin{bmatrix} \Delta V_{in} \\ \frac{D}{T_s} \end{bmatrix}
\]

Since,

\[
\begin{align*}
\frac{d i}{dt} &= \frac{2i_{in}}{d_1 T_s} \left( 1 - \frac{v_C}{v_{in}} \right) + \frac{d_1 v_C}{L} \\
\end{align*}
\]

\[
\frac{d i}{dt} = \frac{2i_{in}}{d_1 T_s} \left( 1 - \frac{v_C}{v_{in}} \right) + \frac{d_1 v_C}{L}
\]

IV. FULL ORDER AVERAGE MODEL FOR BOOST SWITCHING REGULATOR

A reduced-order model can correctly only predict DC and low frequency behavior of PWM converters. However, at high frequencies, it is unable to capture the dynamics of the converter. Hence, a full-order model is also desired.

The full-order derivation starts from a modified averaged model (\( \Delta l_t, \Delta v_C, \Delta v_{in} \)). This model differs from the reduced-order one in terms of duty ratio constraint. From (25), the following relationship is obtained:

\[
l_{pk} = \frac{v_{in}}{L} d_1 T_s
\]

Substituting this into (11), this will result in a duty constraint

\[
d_1 = \frac{2L T_s}{d_0 T_s T_{in}} - d_0
\]

This constraint is different from the earlier one which is derived for a reduced-order model showing that it enforces correct average charging of output capacitor. Putting \( d_2 \) into (21), the following relations are derived:

\[
\frac{di}{dt} = \frac{2i_{in}}{d_2 T_s} \left( 1 - \frac{v_C}{v_{in}} \right) + \frac{d_2 v_C}{L}
\]
\[
\frac{dv_c}{dt} = \frac{i_c}{C} - \frac{d^2i_v}{2LC} - \frac{v_c}{RC}
\]

Equating (35) and (36) to zero and finding the solutions for \(i_c\) and \(v_c\), the DC-operating point can be obtained. Let the scalar value of \(M\) be the output to input voltage ratio. Thus,

\[
M = \frac{v_c}{v_{in}} = \frac{1}{2} + \frac{1}{2} \sqrt{1 + \frac{2D^2R}{L^2} - \frac{2D^2T_s}{L^2}}
\]

Now applying standard linearization techniques and applying small perturbations as follows to (35) and (36):

\[
i_c = i_{c0} + \Delta i_c,
\]

\[
v_c = v_{c0} + \Delta v_c,
\]

\[
v_{in} = v_{in0} + \Delta v_{in},
\]

\[
\Delta = \Delta D + \Delta D;
\]

Then,

\[
d(U_c + \Delta U_c) = \frac{2(U_{c0} + \Delta U_c)}{(D + \Delta D)T_s} - \frac{D + \Delta D}{L} \frac{V_{c0} + \Delta V_c}{(D + \Delta D)T_s}
\]

And

\[
d(U_{in} + \Delta U_{in}) = \frac{U_{in0} + \Delta U_{in}}{2LC} - \frac{(D + \Delta D)T_s}{L} \frac{V_{in0} + \Delta V_{in}}{2LC}
\]

Now, the small signal model can be derived with the following equation:

\[
\frac{d}{dt} \begin{bmatrix} \Delta i_c \\ \Delta v_c \\ \Delta v_{in} \end{bmatrix} = A \begin{bmatrix} \Delta i_c \\ \Delta v_c \\ \Delta v_{in} \end{bmatrix} + B \begin{bmatrix} \Delta u_c \end{bmatrix}
\]

Where,

\[
A = \begin{bmatrix} 2(1 - M) & -D & \frac{MD}{L} \\ -1 & -D & \frac{2M}{L} \\ -D^2T_s & -D^2T_s & -D^2T_s \end{bmatrix}
\]

and

\[
B = \begin{bmatrix} \frac{DT_s}{L} \\ \frac{1}{C} \\ \frac{L}{2LC} \\ \frac{L}{L} \end{bmatrix}
\]

Since,

\[
\begin{bmatrix} \Delta i_c \\ \Delta v_c \\ \Delta v_{in} \end{bmatrix} = \begin{bmatrix} \Delta u_c \end{bmatrix}
\]

\[
\frac{\Delta i_c}{\Delta u_c} = \frac{1}{\Lambda}
\]

Hence, the following transfer functions can be formulated from the small signal model:

\[
\frac{\Delta i_c}{\Delta u_c} = \frac{\frac{sM^2}{L(\Delta - 1)} + \frac{MD}{L}}{\frac{MD}{L}}
\]

\[
\frac{\Delta v_c}{\Delta u_c} = \frac{\frac{sM^2}{L} + \frac{MD}{L}}{\frac{MD}{L}}
\]

V. PARASITIC REALIZATION IN THE BOOST SWITCHING REGULATOR

Due to the difficulties faced during the modelling procedure of the Boost converter, parasitic elements such as conduction voltages, conduction resistances, inductor resistances and equivalent series resistances (ESR) of capacitors have been ignored. The idea of simply considering ideal/lossless components and leaving out parasitic elements simplifies the model development procedure and allows to understand the fundamental behaviour of the switching converter system. However, the effects of parasitic elements and losses are important for improving model accuracy, studying efficiency and dynamic performance of the system. The problem with including the parasitic elements is that they lead to nonlinear current and voltage waveforms and hence result in complications in the modelling procedure.

A Boost converter circuit, with parasitics included, will look like as shown in Fig.6.
During the ON-state:

\[
\begin{bmatrix}
\frac{dI_L}{dt} \\
\frac{dV_i}{dt}
\end{bmatrix}
= \begin{bmatrix}
\frac{L}{R_L + R_{sw}} & 0 \\
0 & \frac{1}{C(R_o + C)}
\end{bmatrix}
\begin{bmatrix}
I_L \\
V_i
\end{bmatrix}
+ \frac{1}{A_2}V_{in}
\]

During the OFF-state:

\[
\begin{bmatrix}
\frac{dI_L}{dt} \\
\frac{dV_i}{dt}
\end{bmatrix}
= \begin{bmatrix}
\frac{L}{R_L + R_{sw}} & 0 \\
\frac{1}{C(R_o + C)} & \frac{1}{R_C}
\end{bmatrix}
\begin{bmatrix}
I_L \\
V_i
\end{bmatrix}
+ \frac{1}{A_2}V_{in}
\]

During DCM period:

\[
\begin{bmatrix}
\frac{dI_L}{dt} \\
\frac{dV_i}{dt}
\end{bmatrix}
= \begin{bmatrix}
\frac{L}{R_L + R_{sw}} & 0 \\
\frac{1}{C(R_o + C)} & \frac{1}{R_C}
\end{bmatrix}
\begin{bmatrix}
I_L \\
V_i
\end{bmatrix}
+ \frac{1}{A_2}V_{in}
\]

Now, applying the averaging technique, this will result in:

\[\dot{x} = [A_1A_2 + A_2A_3 + A_3(1 - d_1 - d_2)]x\]

So, the SSA model for the above equation is

\[
\begin{bmatrix}
\frac{dI_L}{dt} \\
\frac{dV_i}{dt}
\end{bmatrix}
= \begin{bmatrix}
\frac{-d_1+L}{L} & \frac{d_2R_L}{L} & \frac{d_3R_C}{L} & \frac{-d_2}{C(R_o + C)} \\
\frac{d_2R_o}{C(R_o + C)} & \frac{1}{L} & \frac{1}{L} & \frac{-d_2}{C(R_o + C)}
\end{bmatrix}
\begin{bmatrix}
I_L \\
V_i
\end{bmatrix}
+ \frac{1}{A_2}V_{in}
\]

Since there is only one inductor, the x is of dimension two, the correction matrix K is simply given by

\[
K = \begin{bmatrix}
\frac{1}{d_2 + d_3} & 0 \\
0 & 1
\end{bmatrix}
\]

With this correction matrix, the average modified model becomes

\[
\dot{x} = [A_1A_2 + A_2A_3 + A_3(1 - d_1 - d_2)]x
+ [B_1A_2 + B_2A_3 + B_3(1 - d_1 - d_2)]u
\]

Hence, the modified average model of Boost converter in DCM will look like:

\[
\begin{bmatrix}
\frac{dI_L}{dt} \\
\frac{dV_i}{dt}
\end{bmatrix}
= \begin{bmatrix}
\frac{-d_1+L}{L} & \frac{d_2R_L}{L} & \frac{d_3R_C}{L} & \frac{-d_2}{C(R_o + C)} \\
\frac{d_2R_o}{C(R_o + C)} & \frac{1}{L} & \frac{1}{L} & \frac{-d_2}{C(R_o + C)}
\end{bmatrix}
\begin{bmatrix}
I_L \\
V_i
\end{bmatrix}
+ \frac{1}{A_2}V_{in}
\]

In the above state-space equation, replacing d_2 by the duty constraint (34), this will result in

\[\dot{x} = \frac{2I_L}{d_2T_sV_{in}}\text{(35)}\]
\[
\begin{align*}
\mathbf{A} &= \begin{bmatrix}
\frac{A_2}{V_{in}} & \frac{A_3}{V_{in}} \\
\frac{A_4}{V_{in}} & -\frac{1}{V_{in}}
\end{bmatrix} \quad \text{and} \\
\mathbf{B} &= \begin{bmatrix}
\frac{A_3}{V_{in}} \\
\frac{-A_4}{V_{in}}
\end{bmatrix}
\end{align*}
\]

Hence, the following transfer functions can be formulated from the small signal model:

\[
\frac{v_c(s)}{v_{in}(s)} = \frac{1}{\Delta} \left[ s + \frac{1}{C(R_c + R_o)} \left( \frac{A_3}{V_{in}} \right) + \frac{D^2 T_v R_v A_2}{2 C D V_{in} \left( R_{Lc} + R_o \right)} \right]
\]

\[
\frac{\dot{v}_c(s)}{v_{in}(s)} = \frac{1}{\Delta} \left[ s + \frac{1}{C(R_c + R_o)} \left( \frac{A_3}{V_{in}} \right) + \frac{-D T_v V_{in} R_v A_2}{2 C D V_{in} \left( R_{Lc} + R_o \right)} \right]
\]

Since,

\[
Y(s) = C(s) \cdot \sigma(s)
\]
Hence, two transfer functions can be found as follows:

\[
\begin{align*}
\frac{V_o(s)}{D(s)} &= \frac{1}{\Delta} \frac{1}{\left(1 + R_C/R_0\right)} \left(\frac{s + \frac{1}{C(R_C + R_0)}}{\frac{s}{D_{Vin}} - \frac{s}{D_{Vcm}}}\right)
\end{align*}
\]

**VI. SIMULATION RESULTS**

The simulations were done by using the following model parameters: \(L = 18 \text{ H} \), \(C = 4.7 \text{ F} \), \(V_{in} = 5 \text{ V} \), \(D = 0.7 \), \(R_s = 0.1 \Omega \), \(R_i = 0.15 \), \(R_L = 0.8 \), \(R_w = 0.17 \) and \(f_s = 350 \text{ kHz} \). The simulations were performed for two different cases.

Figures 7 and 8 show the Bode diagrams for the transfer function representing the relation between the output voltage and duty ratio (d) (i.e. Control transfer function) for ideal and non-ideal DC-DC Boost converters in DCM, respectively.

As seen from Fig.7 and Fig.8, there is a remarkable change in the open loop gain as the load resistance decreases. When the load resistance goes below a certain range, the open loop gain goes under 0 dB which makes the converter ineffective without an external feedback controller.

Figures 9 and 10 show the transient step responses for ideal and non-ideal DC-DC Boost converters in DCM respectively. As indicated in Fig.9 and Fig.10, these figures were plotted by using two different load values to see the transient behavior as well as the difference between the ideal and non-ideal boost converter. The step response for the non-ideal DC-DC Boost converter is looking identical to the step response for the non-ideal DC-DC Boost converter. This indicates that the ideal and non-ideal transfer functions should be approximately equal; which, in turn, reveals that the loss elements found in the non-ideal DC-DC Boost converter circuit have little effect on the dynamic response of the DC-DC Boost converter operating in DCM.
VIII. CONCLUSION
First, the various aspects of average modelling of DC-DC Boost converter operating in DCM are studied. Basically, the modelling procedure consists of three steps:
1. Averaging the matrix parameters and selection of the correction matrix (K) depending on the number of inductor currents of the converter.
2. Conversion of state-space equations into differential equations for inductor current and capacitor voltage.
3. Defining a duty ratio constraint so that the expression consists of only one duty ratio.
Second, the reduced- and full-order average models have been derived. It was found that the reduced-order model can estimate the behavior in the low frequency range but the full-order model, since dynamics of inductor is present, is more precise.
Finally, various parasitic components have been taken into consideration and a full-order model is developed. The system dynamic behavior for the DC-DC Boost converter with ideal components and DC-DC Boost converter with non-ideal components operating in DCM are compared via Bode plots and transient step responses under different values of the load resistance in order to help in designing a robust controller.

REFERENCES

Haytham Abdelgawad (S’15), was born in Cairo, Egypt, in 1978. He received the B.Sc. (Eng.) and M.Sc. degrees in electrical engineering from Helwan University, Cairo, Egypt, in 2000 and 2009, respectively. He is currently pursuing the Ph.D. degree in electrical engineering at the Faculty of Engineering and Applied Science, University of Ontario Institute of Technology (UOIT), Oshawa, ON, Canada.
His research interests include power converter topologies and their control aspects, Maximum Power Point Tracking of photovoltaic systems, Artificial Intelligence and Optimization Algorithms and the integration of renewable energy systems into the smart grid.

Vijay Sood (SM’79–F’06) received the Ph.D. degree from the University of Bradford, Bradford, U.K., in 1977.
He is currently an Associate Professor and the OPG Design Co-chair at UOIT, Oshawa, Ontario. He has extensive experience in the simulation of HVDC-FACTS systems and their controllers. He has authored two textbooks on HVDC Transmission. His research focuses on the monitoring, control, and protection of power systems and on the integration of renewable energy systems into the smart grid.
Dr. Sood is a Registered Professional Engineer in the province of Ontario. He is a Fellow of the IEEE, Engineering Institute of Canada and the Canadian Academy of
Engineering. He served previously as a Director of the IEEE Canadian Foundation and is a former Editor of the IEEE Transactions on Power Delivery, and Co-editor of the IEEE Canadian Journal of Electrical and Computer Engineering.