

Effect of Transmission Line Capacitance on Performance and Cost of Dynamic Voltage Restorer DVR

Noura G. EL Sayed¹, Gaber El-Saady², El-Nobi A. Ibrahim³, and Mansour A .Mohamed⁴
Electrical Engineering Department, Faculty of Engineering, Assuit University, Assuit, Egypt
nourgaber86@gmail.com, Gaber1@yahoo.com, nouby60@yahoo.com, mamohamed2004@yahoo.com

Abstract- This paper analyzes the Effect of Transmission line Capacitance on Dynamic Voltage Restorer (DVR) Performance. The DVR is used to improve the system voltage profile for different types of faults. The DVR is modeled as three individual units controlled using PWM technique and conventional PI controllers. To approve the DVR effectiveness, symmetrical and unsymmetrical faults are applied at the load point of common coupling. three lengths of transmission line were used to study the effect of transmission line capacitances on DVR performance. Finally, total cost of proposed DVR model components are calculated. All simulations are performed using MATLAB/Simulink to approve the proposed system validity.

Keywords- capacitor, dynamic voltage restorer, voltage sag, swell, pi controller, pulse width modulation.

I. INTRODUCTION

Power quality is one of the main problems in power system. There are many problems of power quality such as voltage transient, harmonic, unbalance voltage, voltage sag and swell [1]. Voltage sag and swell are considered to be one of the most severe disturbances to industrial equipment's [2]. Voltage sags and swells caused by symmetrical three phase faults and unsymmetrical faults such as double line to ground, line to line and single line to ground fault [3]. For some critical devices, a temporary disturbance can cause system crashes, scrambled data, equipment failure and interrupted communications [4]. Voltage sag is defined as reduction in rms voltage to value from 0.1 to 0.9 p.u and for period from 0.1 to 1 minute. Also voltage swell is defined as increase in rms current or voltage at power frequency to value between 1.1 and 1.8 p.u for time from 0.1 to 1 minute [5]. To solve voltage sag and swell problems, dynamic voltage restorer is a modern and important custom power device for mitigation voltage sags and swell in distribution systems [6]. DVR is one of the most efficient and effective custom power devices due to its smaller size and fast response lower cost [7]. DVR is a recently presented series connected solid state device which injects voltage into the system in order to regulate the load side voltage. It is installed in a distribution system between the supply and the critical load feeder at the point of common coupling (PCC) [8]. The Effect of Transmission line Capacitance on Dynamic Voltage Restorer DVR Performance is studied. Transmission line classify to three type short,

medium and long transmission line. Length in transmission line less than 80 km so Capacitance effect is negligible. For medium transmission line length of lines more than 80 km and below 250 km so shunt capacitance must be considered. In case the lines are more than 250 km long the lines are called long transmission line [9].

II. PRINCIPLE OPERATION OF DVR

DVR consists of Harmonic filter, Voltage Source Converter (VSC), Storage Devices, Injection transformer, DC charging circuit, and Control and Protection system [10].

The main function of the DVR is to inject a controlled voltage produced by a forced commuted converter in a series to the bus voltage by means of an injecting transformer [11]. A DC to AC inverter regulates this voltage by sinusoidal PWM technique. All through normal operating condition, the dynamic voltage restorer injects only a small voltage to mitigate for the voltage drop of the device losses and injection transformer [12]. Note that the DVR capable of absorbing or generating reactive power but the active power injection of the device should be provided by an external energy source or energy storage system [12].

The contribution of this paper is that, it studies and analyzes the effect of transmission line Capacitance on Dynamic Voltage Restorer (DVR) Performance. Also total cost of proposed DVR model components are calculated.

III. MODELING OF DYNAMIC VOLTAGE RESTORER

Figure (1) shows modeling of DVR and studies the Effect of Transmission line Capacitance on DVR Performance when short circuit fault occurred on load bus. The DVR model is designed for each phase to measure single phase voltage. the introduced DVR consists of three blocks of PI Controller, and three blocks of single phase voltage source converter.

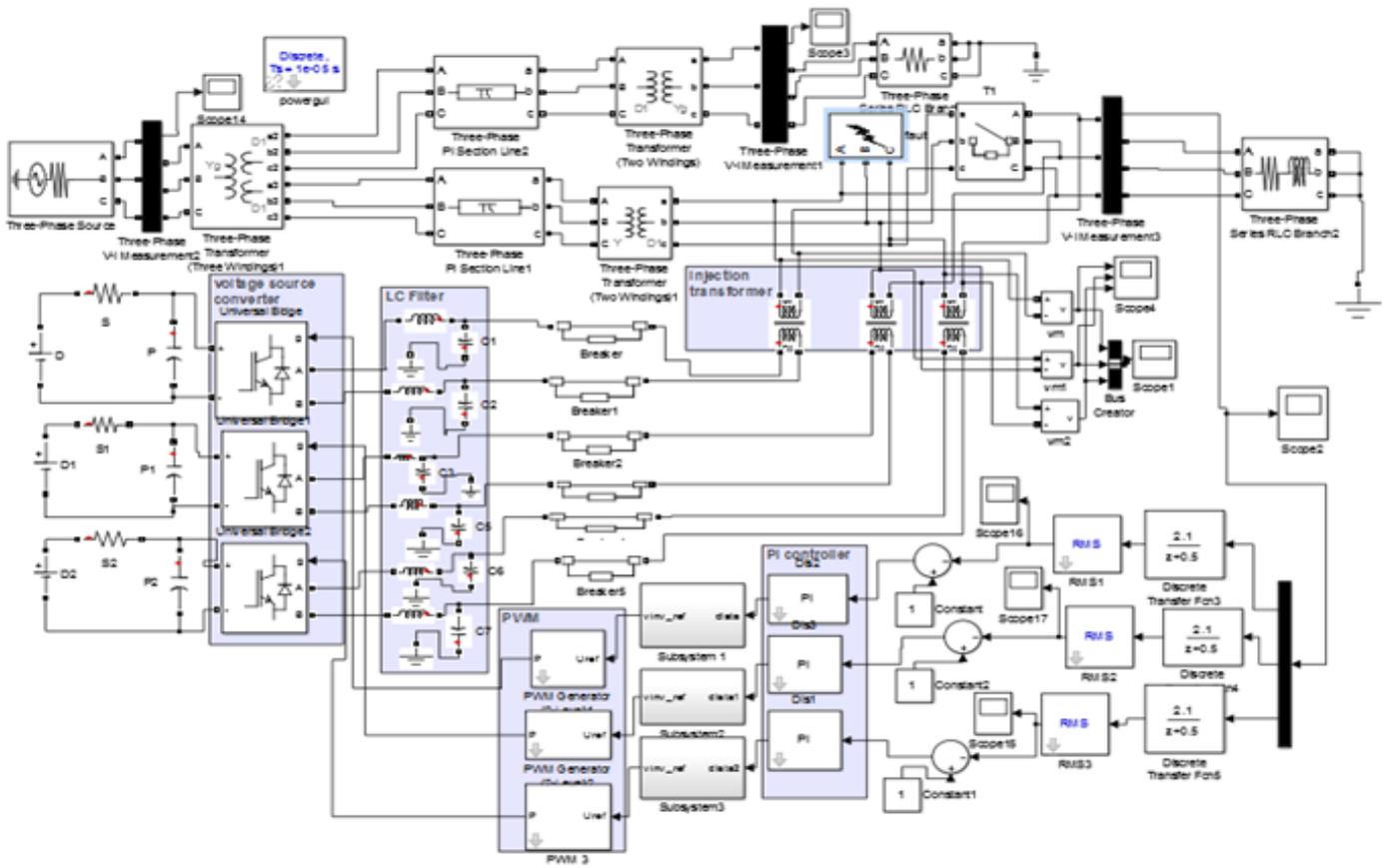


Figure (1): Simulation model with DVR

A. SIMULATION RESULTS AND DISCUSSIONS

Figure (2) presents single line diagram of test system used to study the performance of proposed dynamic voltage restorer. Test system of DVR is composed by 13 kV, 50 Hz generation system, supply two transmission lines through 3-winding transformer connected in Y/Δ/Δ, 13/115/115 kV. Such transmission lines supply two distributions networks through two transformers are connected in Δ/Y, 115/11 kV. The simulations are done as follows.

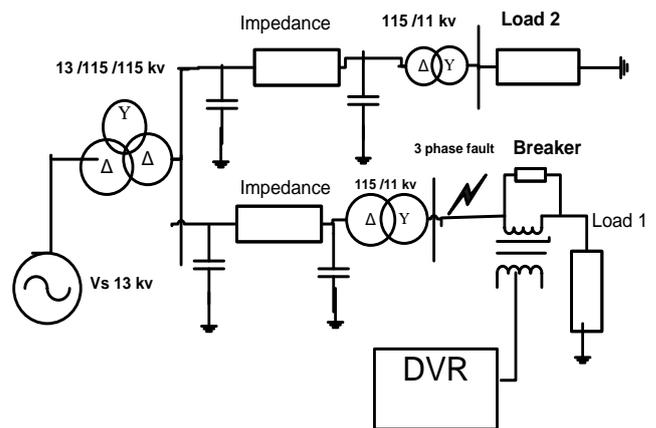


Figure (2): Single line diagram of test system

a) Three phase fault without DVR

In this model three phase short circuit fault is studied, three phase fault is applied at load point, and it has fault resistance of 0.33 ohms during the period 0.2 to 0.3 sec. It is found that voltage sag at phase A, B, C in Figure (3) are equal. as shown in table1.

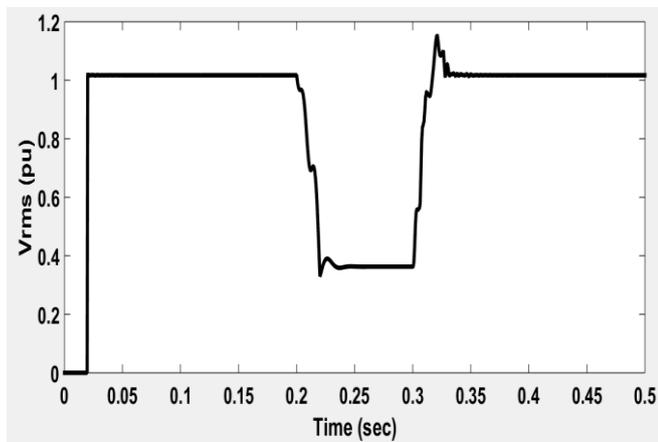


Figure (3): RMS load voltage at phase (A,B,C) with three phase fault without DVR, With L=200km

Table 1: The different length of line in case of 3- Φ fault without DVR

The Length of line (km)	Out DVR sag magnitude %
100	50%
200	64%
230	67%

b) Three phase fault with DVR

The voltage sag is mitigated as shown in Figure (4) when DVR is connected to model. Also the required DC voltage increase with increasing the length of the line as shown in Table 2.

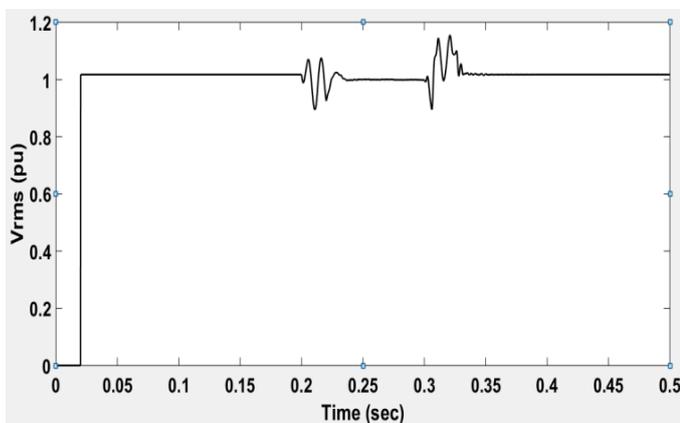


Figure (4): RMS voltage at phase (A,B,C) with three phase fault with DVR, with L=200km

Table 2: The different length of line with 3- Φ fault, with DVR

The length of line (km)	Required DC _{new} Voltage	With DVR sag magnitude %
100	5kv	100
200	5.6kv	99
230	5.8kv	99

c) single line to ground fault without DVR

Single line to ground fault is applied at load point. It has fault resistance of 0.77 ohms during the period 0.2 to 0.3 sec, the voltage sag and swell occur when DVR is not connected as shown in Figure (5,6,7) and table 3. Voltage profile increases with increasing the length of linephases.

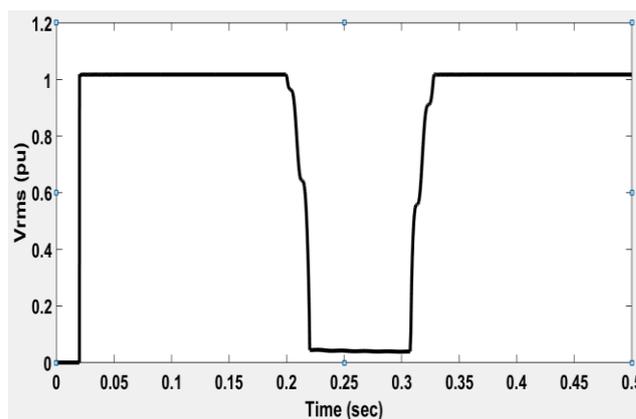


Figure (5): RMS load voltage at phase A with single line to ground fault without DVR, with L=200km

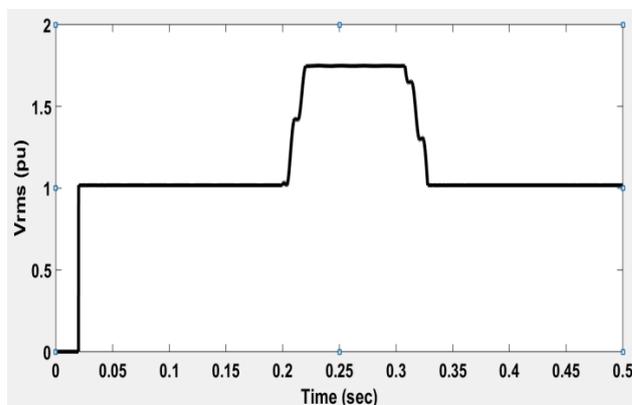


Figure (6): RMS load voltage at phase B with single line to ground fault without DVR, with L=200km

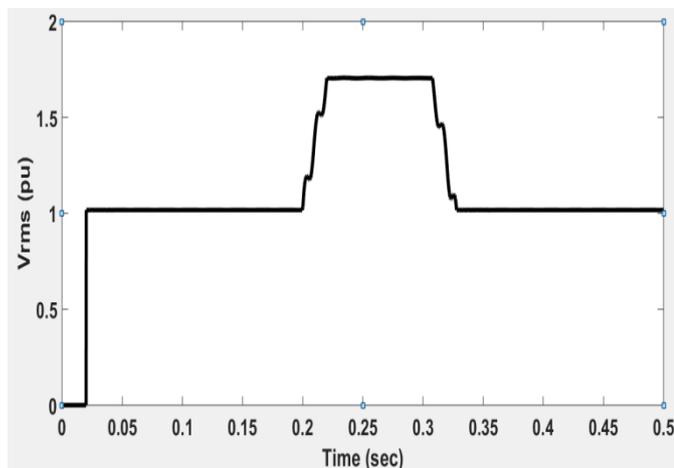


Figure (7): RMS load voltage at phase C, with single line to ground fault without DVR, with L =200km

Table 3: The different lengths of line with single line to ground fault, without DVR

The length of line (km)	Sag in Phase A %	Swell in Phase B %	Swell in Phase C %
100	96	71	68
200	96	75	70
230	96	78	72

d) Single line to ground fault with DVR

The voltage sag and swell are eliminated When DVR is connected to model, as shown in Figure (8,9,10). The value of required DC voltage not change with increasing the length of line because the increasing in voltage swell is small. And voltage sag value not change as shown in table 4.

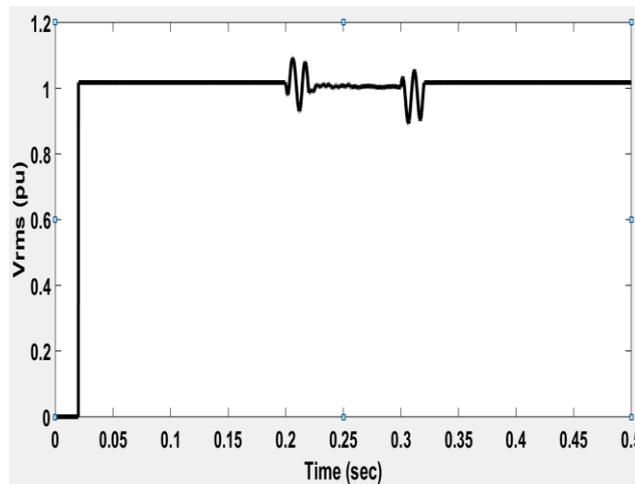


Figure (8): RMS load voltage at phase A, with single line to ground fault with DVR, with L =200km

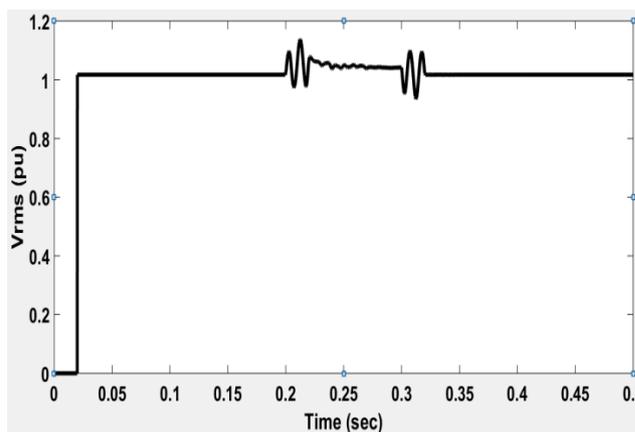


Figure (9): RMS load voltage at phase B, with single line to ground fault with DVR, with L=200km

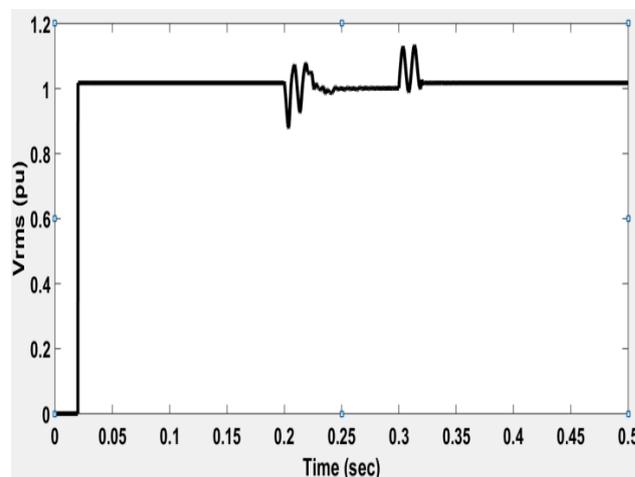


Figure (10): RMS load voltage at phase C, with single line to ground fault with DVR, with L=200km

Table 4 The different lengths of line with single line to ground fault, with DVR

The length of line	Sag in Phase A %	Swell in phase B%	Swell in phase C %	Required DC voltage
100	100	100	100	VdcA =7.8KV vdcB=12kv vdcC=17kv
200	100	100	99	
230	98	100	99	

e) Line to line fault (A, B) without DVR

A Line to line fault is applied at load point.it has fault resistance with 0.77 ohms during the period 0.2 to 0.3 sec. the voltage sag takes place when dynamic voltage restorer is not connected.the voltage sag takes place at phase B in Figure (12) as shown in table 5 . But load voltage at phase {A, C} in Figure (11,13) is equal to reference voltage. also voltage profile increases.

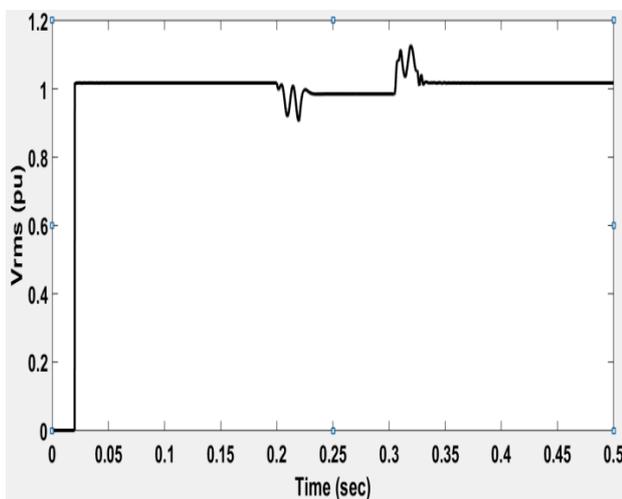


Figure (11): RMS load voltage at phase A, with line to line fault without DVR, with L=200km

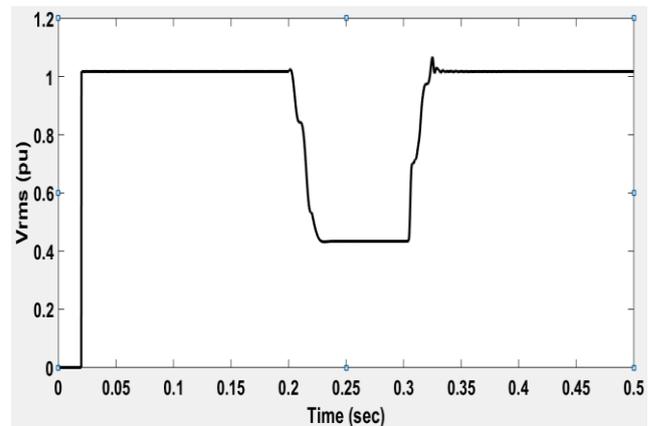


Figure (12): RMS load voltage at phase B, with line to line fault without DVR, with L=200km

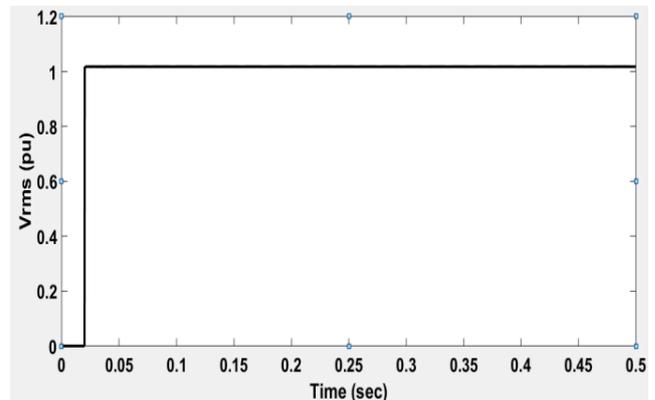


Figure (13): RMS load voltage at phase C, with line to line fault without DVR, with L=200km

Table 5: The different lengths of line with line to line fault, without DVR

The length of line (km)	Out DVR Sag in phase B %
100	42%
200	53%
230	55%

f) Line to line fault (A, B) with DVR

The voltage sag at phase B in Figure (14) is mitigated when DVR is connected to model as shown in table 6. when the length of line increases the required DC voltage of DVR reduce.

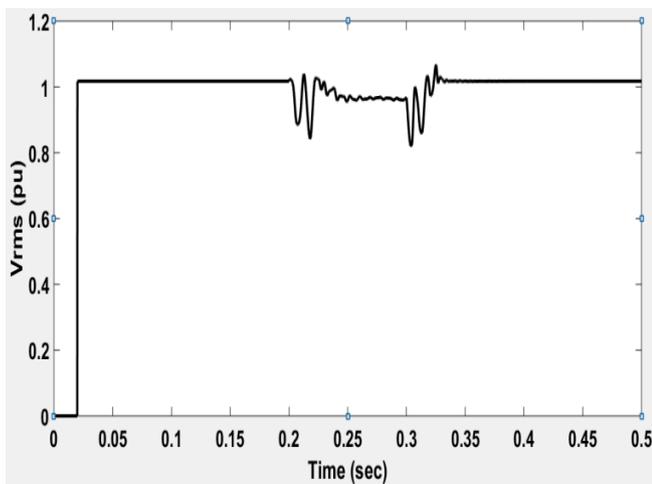


Fig (14) RMS load voltage at phase B, with line to line fault with DVR, with L=200km

Table 6 : The different lengths of line with line to line fault, with DVR

The length of line (km)	Phase A%	Sag in Phase B%	Phase C%	Required DC _{new} Voltage (kv)
100	100%	99%	100%	$V_{dcA}=3kv,$ $V_{dcB}=10.8kv,$ $V_{dcC}=16.5kv$
200	100%	97%	99%	$V_{dcA}=1.6kv, V_{dcB}=10.5kv,$ $V_{dcC}=16.8KV$
230	99%	96%	98%	$V_{dcA}=1.5kv,$ $V_{dcB}=10.2kv,$ $V_{dcC}=17kv$

g) Double line to ground without DVR

A double line to ground fault is applied at load point. it has fault resistance with 0.77 ohms during the period 0.2 to 0.3 sec. the voltage sag and swell occur in Fig (15,16,17) when DVR is not connected. as shown in table 7.

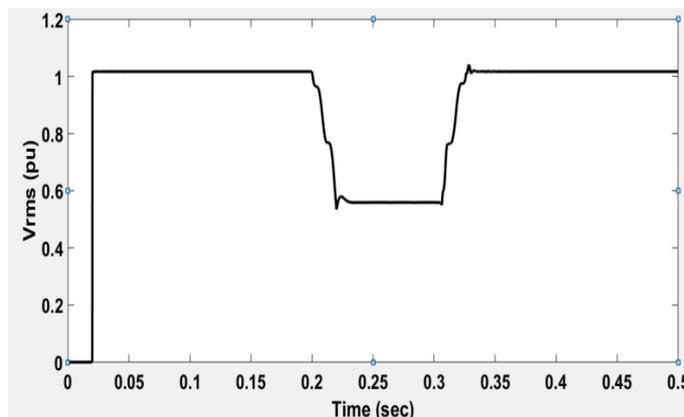


Fig (15) RMS load voltage at phase A, with double line to ground fault without DVR, with L=200km

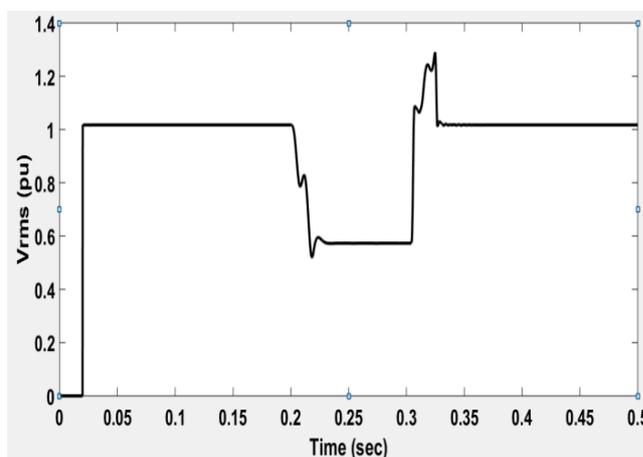


Fig (16) RMS load voltage at phase B, with double line to ground fault without DVR, with L =200km

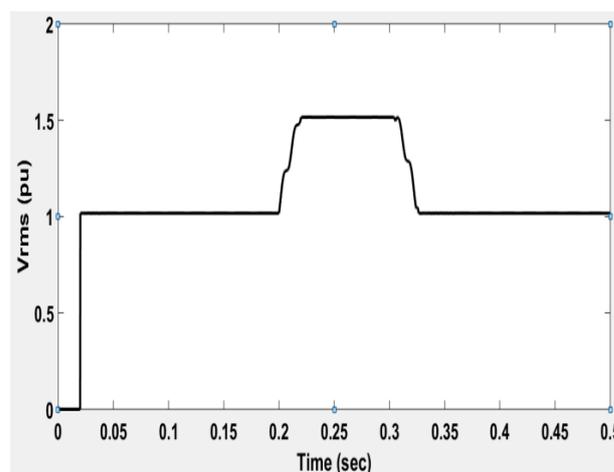


Fig (17) RMS load voltage at phase C, with double line to ground fault without DVR, with L=200km

Table 7: The different lengths of line with double line to ground fault, without DVR

The length of line(km)	Sag in phase A %	Sag in phase B %	Swell in phase C %
100	35%	35%	48%
200	45%	43%	51%
230	47%	47%	53%

h) Double line to ground without DVR

The voltage sag and swell are eliminated When DVR is connected to model as shown in Figure (18,19,20), the required DC voltage of DVR reduce with increasing the length of line as shown in table 8.

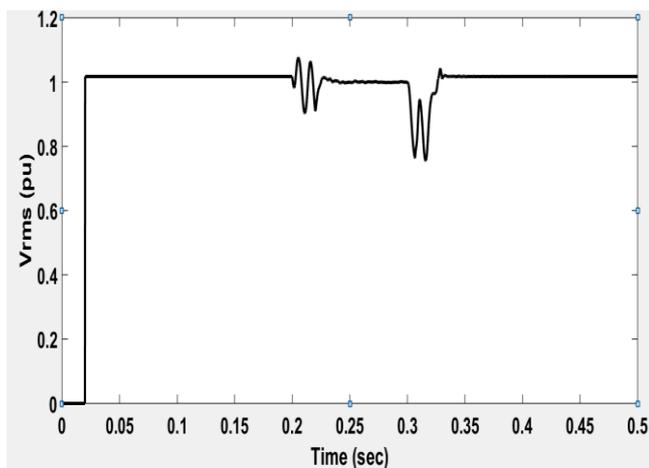


Figure (18): RMS load voltage at phase A, with double line to ground fault with DVR, with L=200km

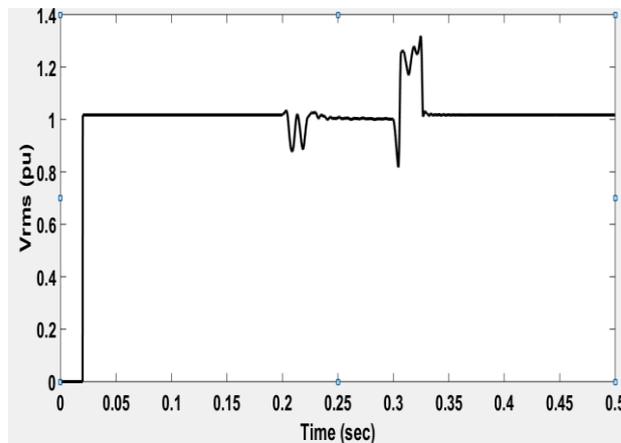


Figure (19):RMS load voltage at phase B, with double line to ground fault with DVR, with L=200km

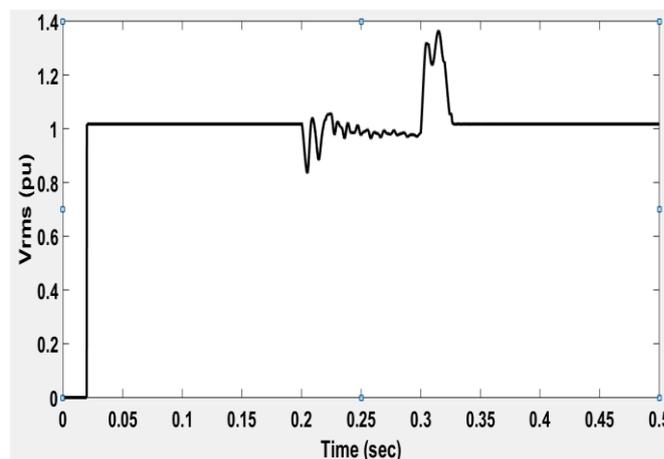


Figure (20): RMS load voltage at phase C, with double line to ground fault with DVR, with L=200km

Table 8: The different lengths of line with double line to ground fault, with DVR

The length of line(km)	Phase A %	Sag in Phase B %	Phase C %	Required DC new Voltage (kv)
100	100%	100%	100%	$V_{dcA}=9.2kv$, $V_{dcB}=7.5kv$, $V_{dcC}=16.5kv$
200	100%	100%	99%	$V_{dcA}=8.4kv$, $V_{dcB}=7kv$, $V_{dcC}=16.5KV$
230	98%	98%	99%	$V_{dcA}=8.2kv$, $V_{dcB}=6.8kv$, $V_{dcC}=16.5kv$

VI. CALCULATIONS OF DVR COST

Assume compensation level for the single-phase load is 50%. the coupling transformer power used in the classical dynamic voltage restorer is calculated as :

$$S_{ta} = S * \frac{N_c}{3} \quad (1)$$

Where (S) is the load apparent power, (Nc) is the compensation level of voltage sag and (Sta) is the apparent power of the single-phase coupling transformer.

from simulation: three phase load apparent power is calculated

$$S_13 (= \sqrt{3} V_1(L) I_1(L)) = 1399982.027 = 1.4Mva \quad (2)$$

$$S_{1ta} = 0.5 * 1399982.027(3) = 233.33 KVA \quad (3)$$

The coupling transformer secondary voltage could be calculated by multiplying the load single-phase voltage by the required compensation level.

$$V_s = N_c * V_L = 0.5 * \frac{11}{\sqrt{3}} = 3175 V \quad (4)$$

The minimal dc link voltage could be calculated as:

$$V_{mdc} = V_s * \sqrt{3} \sqrt{2} * \frac{V_1}{V_2} = 9503.65 V \quad (5)$$

The lowest possible remaining energy would be the logical choice since it limits the capacitor value, but this would result in a high steady state voltage (voltage in which the dynamic voltage restorer does not inject neither absorb energy), effect directly in the cost of the switches and rated voltage. the minimal dc link voltage is known, it is possible to calculate the capacitor value by defining the remaining energy available in the dc link. The remaining energy of 1 MJ was chosen [13].The capacitor value is calculated as:

(6)

The maximum dc link voltages and the steady state can be calculated by using

$$V = \sqrt{\frac{2E}{C}} \quad (7)$$

As the capacitor absorbs 350 kJ until reaching the maximal voltage and injects 350 kJ until reaching the minimal voltage, the steady state and the maximum dc link voltages are, respectively: [11]

$$V_t = \sqrt{\frac{2 * 1350000}{0.0221}} = 11078.23 V \quad (8)$$

$$V_{mdc} = \sqrt{\frac{2 * 1700000}{0.0221}} = 12431.63 V \quad (9)$$

Considering the rated voltage of the switches in function of the market available and price, IGBTs of 1200 V are chosen[14]. Therefore, it is necessary to decrease the maximum dc link voltage, fitting the switches voltages. Adopting a safety margin of 33%, the same margin used in commercial inverters, the maximum dc link voltage has to be decreased to 1,139 V.

To obtain the required decrease, the turns-ratio of the coupling transformer is 1:2 and its primary voltage is calculated as:

$$V_p = V_s * 0.5 = \frac{3175}{2} = 1587.5 V \quad (10)$$

To calculate the current through the IGBTs, the primary current is multiplied by the turns-ratio, obtaining 146.96A.

The coupling transformer has secondary voltage of 3175 V, primary voltage of 1587.5 V and rated power of 466.6 kVA. The power is defined as a double of the power that will be injected by the transformer. This is a safety margin used by the industry to avoid saturation in transient situations that could cause destroy to the dynamic voltage restorer [13]. Note that this transformer introduces special characteristics and it is designed to attend the application requirements, resulting in high cost. Another important characteristic is the low resistance in the windings to reduce losses. The price of the coupling transformer in relation to the conventional transformer is increased in 10% due to impossibility of obtaining the exact cost . The cost includes the filter that constitutes other element in which the characteristics depend on the place where it would be used [13]. Table 9 was built by using the prices obtained in [14].

Table .9 Calculated cost of the DVR

Item	Price (US\$)
H-bridge (3unit)	374.04
Three phase rectifier	1,549.48
Coupling transformer	41,976

Capacitor bank (3 unit)	20,010
Total	63,909.52

VII. CONCLUSION

This paper studies the performance analysis of DVR in presence of transmission line capacitance. Presence of transmission line capacitance increases voltage profile. Different fault types three phase, single line to ground fault and double line to ground are applied at the load bus to examine the powerful of the proposed DVR. In case of three phase fault, The required DC voltage of DVR increases with increasing the length of line while in case of double line to ground fault and line to line fault with DVR the required DC voltage of DVR reduce with increasing the length of line. Also, the total cost of proposed DVR model components are calculated .

VIII. REFERENCES

- [1] Asst. Prof. N.J.dhimmar, Asst. Prof. P.D.Solanki and Asst. Prof. M.P.mishra, “Voltage Sag Mitigation by Dynamic Voltage Restorer, ” Journal of information, knowledge and rsearch in electrical Engineering, ISSN: 0975 – 6736, Volume – 02, Issue – 02, Page 267-270, Nov 12 to Oct 13.
- [2] Ravilla Madhusudan, and G. Ramamohan Rao, *Modeling and Simulation of a Dynamic Voltage Restorer (DVR) for Power Quality Problems Voltage Sags and Swells*, IEEE- International Conference On Advances In Engineering, Science And Management (ICAESM - 2012), PP 442-447, March 30, 31,2012.
- [3] Rasool M. Imran, and Jyoti Srivastava, *Using of Dynamic Voltage Restorer (DVR) to Mitigate Sag and Swell Voltage during Single Line to Ground & Three-Phase Faults*, International Journal Of Modern Engineering Research (IJMER), IJMER , ISSN: 2249–6645 , Vol. 4 , Iss. 2 , Feb. 2014 , pp180-188.
- [4] .Manila Garg¹, Parag Nijhawan², *Mitigation of Power Quality Problems Using DVR in Distribution Network for Welding Load*, IOSR Journal of Electrical and Electronics Engineering (IOSR-JEEE), e-ISSN: 2278-1676,p-ISSN: 2320-3331, Volume 10, Issue 4 Ver. I (July – Aug. 2015), PP 106-112.
- [5] Noura G. EL Sayed, Gaber El-Saady², El-Nobi A. Ibrahim³, and Mansour A .Mohamed⁴, *Dynamic Voltage restorer for enhancing distribution systems power quality*, 2019 7th International Japan-Africa Conference on Electronics, Communications, and Computations, (JAC-ECC), 978-1-7281-5616-3/19/\$31.00c 2019 IEEE 218, pp 218-223.
- [6] Shazly A. Mohammed¹, Aurelio G. Cerrada², Abdel-Moamen M. A1, and B. Hasanin³, *Dynamic Voltage Restorer (DVR) System for Compensation of Voltage Sags, State-of-the-Art Review*, International Journal Of Computational Engineering Research (ijceronline.com) Vol. 3 Issue. 1, January 2013, Page 177-183.
- [7] Anjani Kumar Prajapati and K. P. Singh, *Power Quality Improvement Using DVR (Dynamic Voltage Restorer) Under Various Fault Conditions*, International Research Journal of Engineering and Technology (IRJET), Volume: 04 Issue: 11, pp994-999, Nov -2017.
- [8] Saurabh Sahu and Neelesh Kumar, *Mitigation of Voltage SAG Problem Using Dynamic Voltage Restorer (DVR)*, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 4, Issue 10, pp8308-8318, October 2015.
- [9] John wiley and sons, “*electric power transmission system engineering analysis and design*, ” Book, press 1987.
- [10] Nagesh M and Nandini S Kodihall, “*Comparative Analysis of Performance of D-STATCOM and DVR for Voltage sag, Voltage swell and Fault compensation*, ” International Research Journal of Engineering and Technology (IRJET), Volume: 05, Issue: 06, pp2884-2891, June-2018.
- [11] P.Praveen and Prof. K.Gopinath, “*Novel Optimization Of Dynamic Voltage Restorer to Mitigate Sag and Swell Using Adapative Fuzzy Logic Control*, ” International Journal of Emerging Technology in Computer Science & Electronics (Ijetcse), Volume 20 Issue 3, pp203-213, March 2016.
- [12] G. Balasubramanian, *Design and Implementation of Dynamic Voltage Restorer for Voltage Sag Mitigation*, International Research Journal of Advanced Engineering and Science, Volume 1, Issue 2, pp. 33-39, 2016.
- [13] M. E. C. Brito, M. C. Cavalcanti, L. R. Limongi and F. A. S. Neves, *Low Cost Dynamic Voltage Restorer*, International Conference on Renewable Energies and Power Quality (ICREPQ'12) Santiago de Compostela (Spain), 28th to 30th March, 2012, Vol.1, No.10, PP378-383, April 2012.
- [14] Sindopower GmbH, <http://www.sindopower.com> (access in January 21, 2020).

B. AUTHORS AND AFFILIATIONS

Gaber El-Saady Ahmed Taha born in Aswan, Egypt in 1959. He received his BSc, MSc and Ph.D. from Assuit University, Egypt in 1982 and 1988, and 1995 respectively, all in Electrical Engineering. He was Demonstrator from 1982–1988, he worked as a teaching assistant till 1995. He was a staff member as Associate Professor from 2000, he has been a professor since 2006 at Electrical Engineering Department, Assuit University. He supervised the “Center for Maintenance and Repair of Scientific Appliances” in Assuit University from 2010-2014. His fields of interest include power system analysis, DG systems, modeling, fuzzy logic controller, innovative systems design, phase induction motor, simulated annealing algorithm, applied simulation, and power electronics control applied to renewable energy and power quality systems such as active power filters, PWM rectifiers.



El-Nobi Ahmed Ibrahim was born in Luxor, Egypt in 1960. He received his BSc, MSc and Ph.D. from Assuit University, Egypt in 1984 and 1990, and 1996 respectively, all in Electrical Engineering. He is currently an assistant Member Professor at the Department of Electrical Engineering, Assuit University, Egypt. His main research interests are planning, application of artificial intelligent techniques on power systems, control systems, optimization, analysis, simulation and design of electrical systems.



Mansour A. Mohamed received the BSc and MSc degrees in electrical engineering from Assiut University, Assiut, Egypt, in 1990 and 1996, respectively, and the Ph.D. degree in electrical engineering from Arizona State University, Arizona, United States, in 2003.



Currently he is an Associate Professor in the College of engineering, Assiut University. His main interests and research fields are power transmission and distribution systems, power system protection and power quality.



Noura Gaber Bakhiet Elsayed was born in sohag in 1992. She had her BSc of Engineering 2014 and Diploma in Electrical power and machine for 2years 2014 to 2016 in Electrical Engineering from Assuit University, Egypt.